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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Thomas J. Massingill, et al.
Serial No.: 09/997,589
Filed: November 29, 2001
For: *Multi-Chip Module And Method For
Forming And Method For Deplating
Defective Capacitors*
Group Art Unit: 2827
Examiner: THAI, Luan C.
Atty. Dkt.: 6136/53804 (25916-162)

**CERTIFICATE OF
MAILING/TRANSMISSION
(37 C.F.R. § 1.8A)**

I hereby certify that this correspondence is,
on the date shown below, being:

☒ (X) deposited with the United States Postal
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450.

☐ () transmitted by facsimile to the Patent
and Trademark Office.

8/9/04
Date

Jordan Wilson

**REQUEST UNDER 37 C.F.R. § 3.81(b)
FOR ISSUANCE OF PATENT IN THE NAME OF ASSIGNEE
(FILED AFTER PAYMENT OF ISSUE FEE)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450.

Sir:

In accordance with 37 C.F.R. § 3.81(b), it is respectfully requested that the above
application issue in the name of the Assignee, Fujitsu Limited, and that Assignee's name be
printed on the face of the patent when it issues. Applicant mailed the Issue Fee to the USPTO on
July 23, 2004.

This Request is accompanied by the enclosed fee of \$130.00 in accordance with 37
C.F.R. § 1.17(i). Also enclosed are the executed assignment documents with an attached
Recordation Cover Sheet pursuant to 37 C.F.R. § 3.28 and the requisite recordation fee of \$40.00.
In accordance with § 3.81(b), the Commissioner is directed to record the enclosed assignments.

The Commissioner is hereby authorized to charge any additional fees which may be

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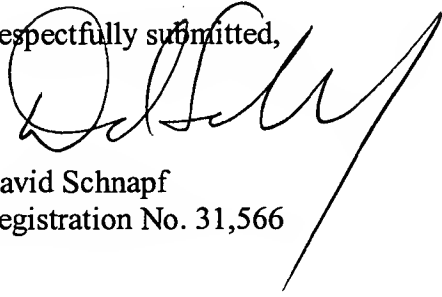
W02-SF:5SD\61424387.1

required in this application or credit any overpayment, to Deposit Account No. 501395. This sheet is filed in duplicate.

August 9, 2004

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Respectfully submitted,


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